

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Simon DELEONIBUS  
SERIAL NO.: unassigned  
FILING DATE: herewith  
TITLE: MIS Transistor with Self-Aligned Gate and Method for Making  
Same  
EXAMINER: UNASSIGNED  
ART UNIT: UNASSIGNED

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COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks / Arguments** begin on page 3 of this paper.